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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/532,643	04/25/2005	Christian Siemers	4001-1201	4379

466 7590 02/10/2006

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EXAMINER
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CRAWFORD, JASON

ART UNIT	PAPER NUMBER
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2819

DATE MAILED: 02/10/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 10/532,643	<b>Applicant(s)</b> SIEMERS, CHRISTIAN	
	<b>Examiner</b> Jason Crawford	<b>Art Unit</b> 2819	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 24 October 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-5 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-5 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**REXFORD BARNIE**  
**SUPERVISORY PATENT EXAMINER**

#### Attachment(s)

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)  | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5 are rejected under 35 U.S.C. 102(b) as being anticipated by Schultz (US 6255848).

In regards to Claim 1, Schultz discloses of a programmable logic device (100) comprising of a number of logic blocks (CLBs) with configurable characteristics which in each case compromise at least one processing unit (inherent) with the function programs and interfaces to the other logic blocks, at least one input/output unit (IOBs) associated with the logic blocks (CLBs), and means for linking the logic blocks (152) to one another, to at least one of the processing units of another logic block (CLBs) and to at least one input/output unit (IOBs) characterized by reconfigurability of the logic blocks (CLBs) during the entire operation of the logic device (100) due to the fact that the linking elements additionally exhibit a configurable changeover logic block (120) by means of which at least some of the reconfigurable logic blocks themselves and/or their connections of the at least one input/output unit (IOBs) are configured. (Column 2 Lines 47-59, Column 4 Lines 61-67, Column 5 Lines 1-36 and Fig 2A)

In regards to Claim 2, Schultz discloses of the changeover logic block (120) is constructed in one plane, which differs from a plane (150) with the reconfigurable logic blocks (CLBs). (Fig 2A)

In regards to Claim 3, Schultz discloses of the two planes (120, 150) being constructed as being largely equivalent in size. (Inherent in Fig 2A)

In regards to Claim 4, Schultz discloses of the device of Claim 1 characterized in that at least some of the reconfigurable logic blocks (CLBs) are configured in accordance with a predetermined context (inherent).

In regards to Claim 5, Schultz discloses of the changeover logic circuit (120) exhibiting at least one state memory (Inherent from state machine 470) which contains information with respect to the functions of the individual reconfigurable logic blocks (CLBs), and that the selected reconfigurable logic blocks (CLBs) are configured in accordance with the function information of the selected state. (Column 5 Lines 27-31, Column 8 Lines 26-34 and Fig 2A, 4)

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jason Crawford whose telephone number is 571-272-6004. The examiner can normally be reached on Monday - Friday 7am-4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rex Barnie can be reached on 571-272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2819

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JMC

  
REXFORD BARNIE  
SUPERVISORY PATENT EXAMINER